

## Designing an IC- Becoming a Power User

In this tight job market, it is essential for all of us to "sharpen (and enhance) our skill set". We recently launched up a startup "Catalyte IC Design"- one of our primary focus is "Trainings on High-end IC design". We'll offer a brief training session at LHH next Tue (5/19). If interested, please sign up. Here's the summary of the training session.

What- Designing an IC- Becoming a Power User

When- May 19 (Tue) - 1 to 2 pm

Where-

Lee Hecht Harrison (LHH)

Bank of America Plano Tower

101 East Park Blvd., Suite 300

Plano, TX 75074

### Intended Audience

1. Anyone involved with some phases of IC design (e.g. - Verification, RTL, Synthesis, Physical design, Layout, Analog and RF design, EDA, PDK) - Expand understanding with **all phases** of the design flow.
2. Anyone involved with semiconductor industry (e.g. - Design, Process, Product and Test Engineering, Library/PDK) - Widen the Horizon

### Outline of the Presentation

- Company overview- Catalyte IC Design (15 min)
- Module1- Designing an IC- Concepts through Implementations (15 min)
- Module2- Custom Design Flow– Default to Power (15 min)
- Q/A (15 min)

### Presenter

Faizul Alam

CTO

Catalyte IC Design

For additional info-

[www.catalyteic.com](http://www.catalyteic.com)