

Designing an IC- Be a Power User

To remain competitive in this tight job market, it is essential for all Semiconductor professionals/ IC Design Engineers to “sharpen their skill set”. We recently launched up a startup "Catalyte IC Design"- one of our primary focuses is "Training on High-end IC design". CatalyteIC will offer a short seminar in Austin, Texas on June 22. Here's the information for the seminar.

What- Designing an IC- Be a Power User

When- June 22nd (Mon), 7 to 8 pm

Where- One Highland Center (4th floor), 314 E. Highland Mall Blvd, Austin, TX 78752

Intended Audience

1. Anyone involved with some phases of IC design (e.g. - Verification, RTL, Synthesis, Physical design, and Layout, Analog and RF design, EDA, PDK) - Expand understanding with **all phases** of the design flow.
2. Anyone involved with semiconductor industry (e.g. - Design, Process, Product and Test Engineering, Library/PDK) - Widen the Horizon

Outline of the Presentation

- Module 1- Designing an IC- Concepts through Implementations
- Module 2- Custom Design Flow– Default to Power
- Module 3- PDK Demystified
- Q/A

Presenter

Faizul Alam
Chief Technology Officer
Catalyte IC Design

For additional info-
www.catalyteic.com